

FIG. 1

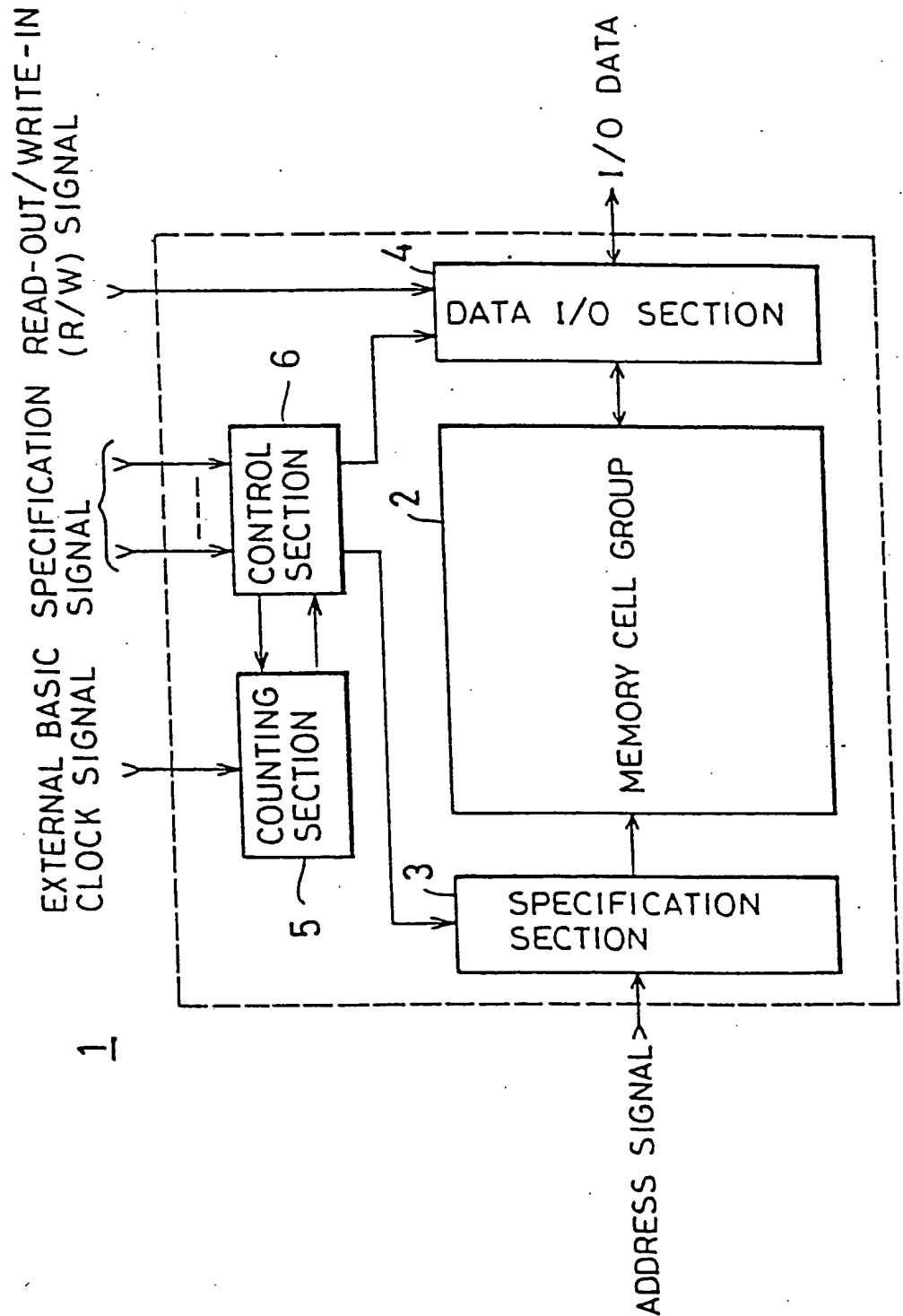


FIG. 2

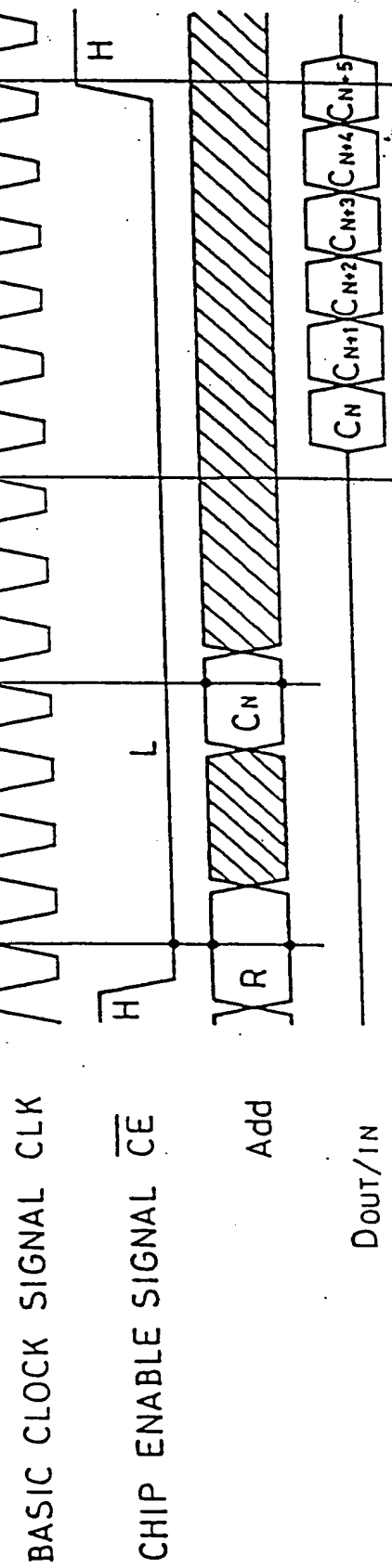


FIG. 3

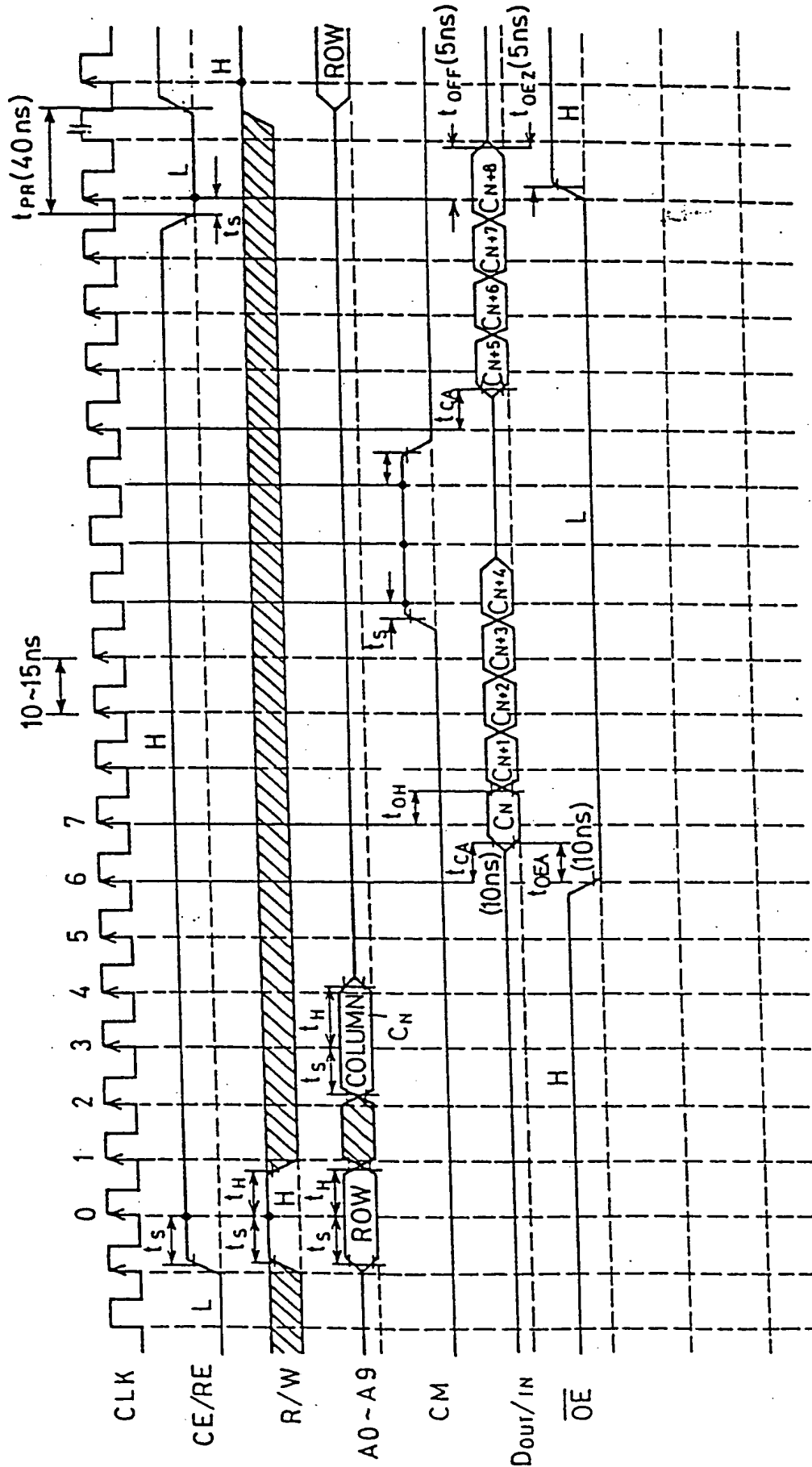


FIG. 4

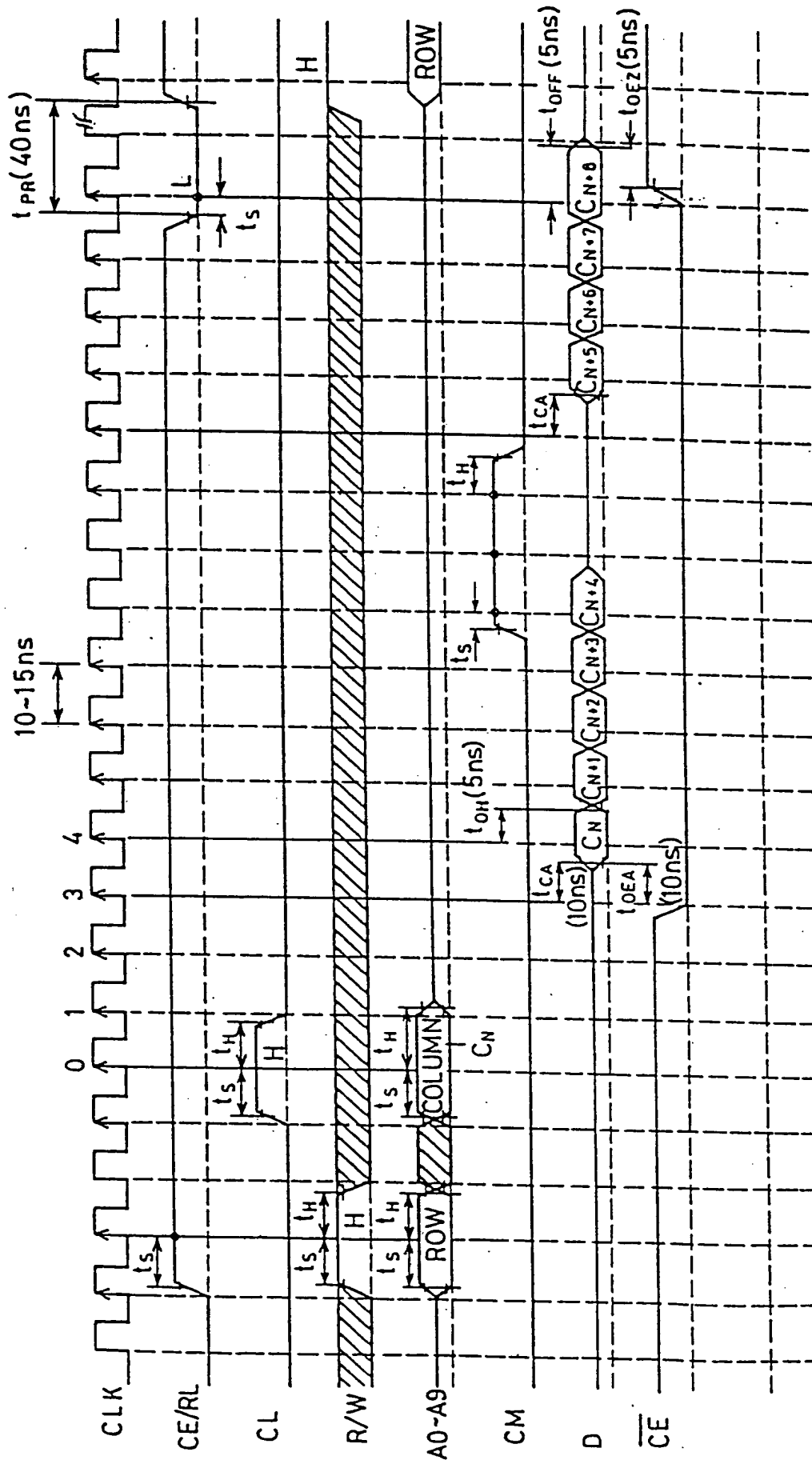


FIG. 5

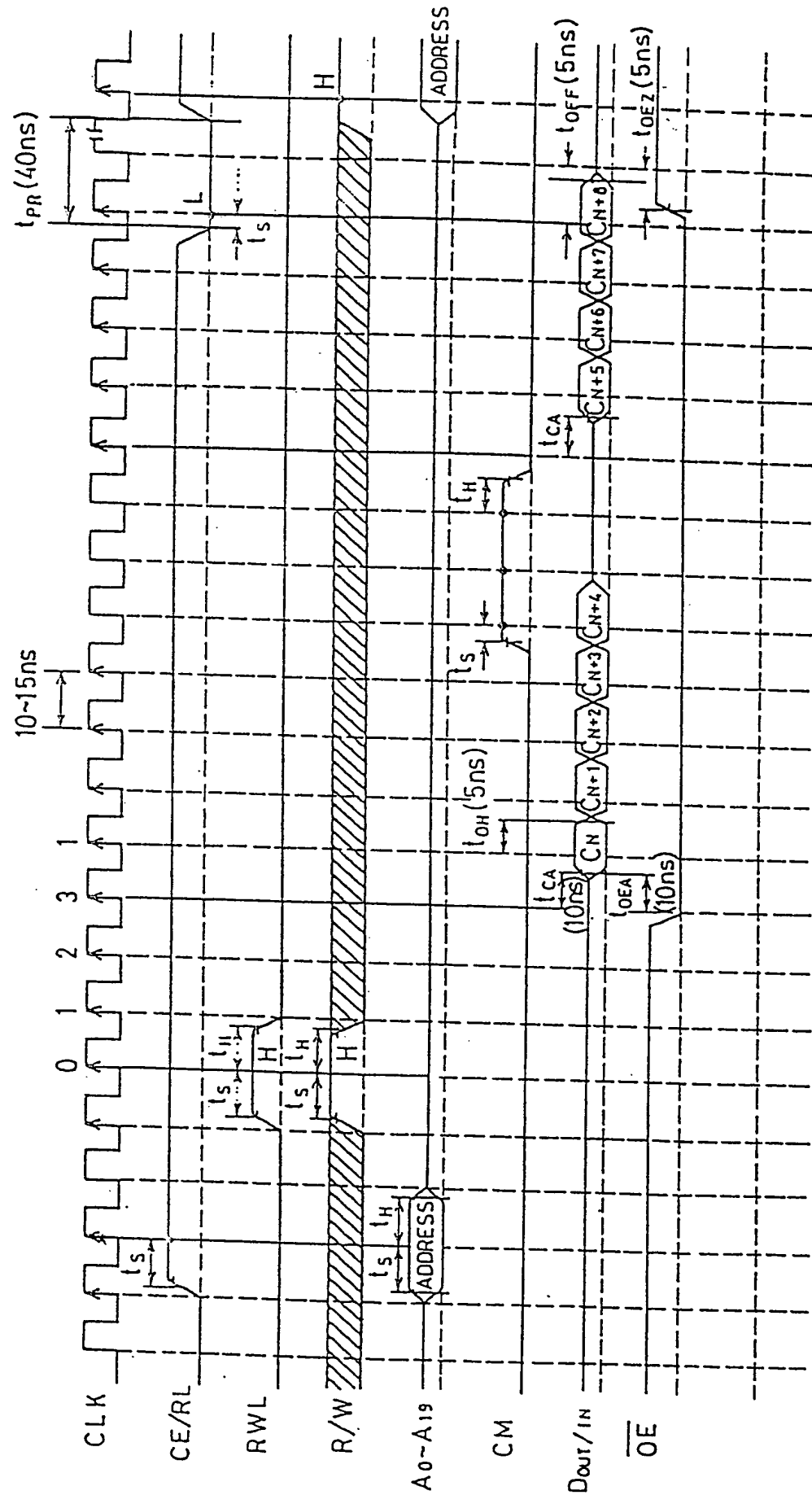


FIG. 6

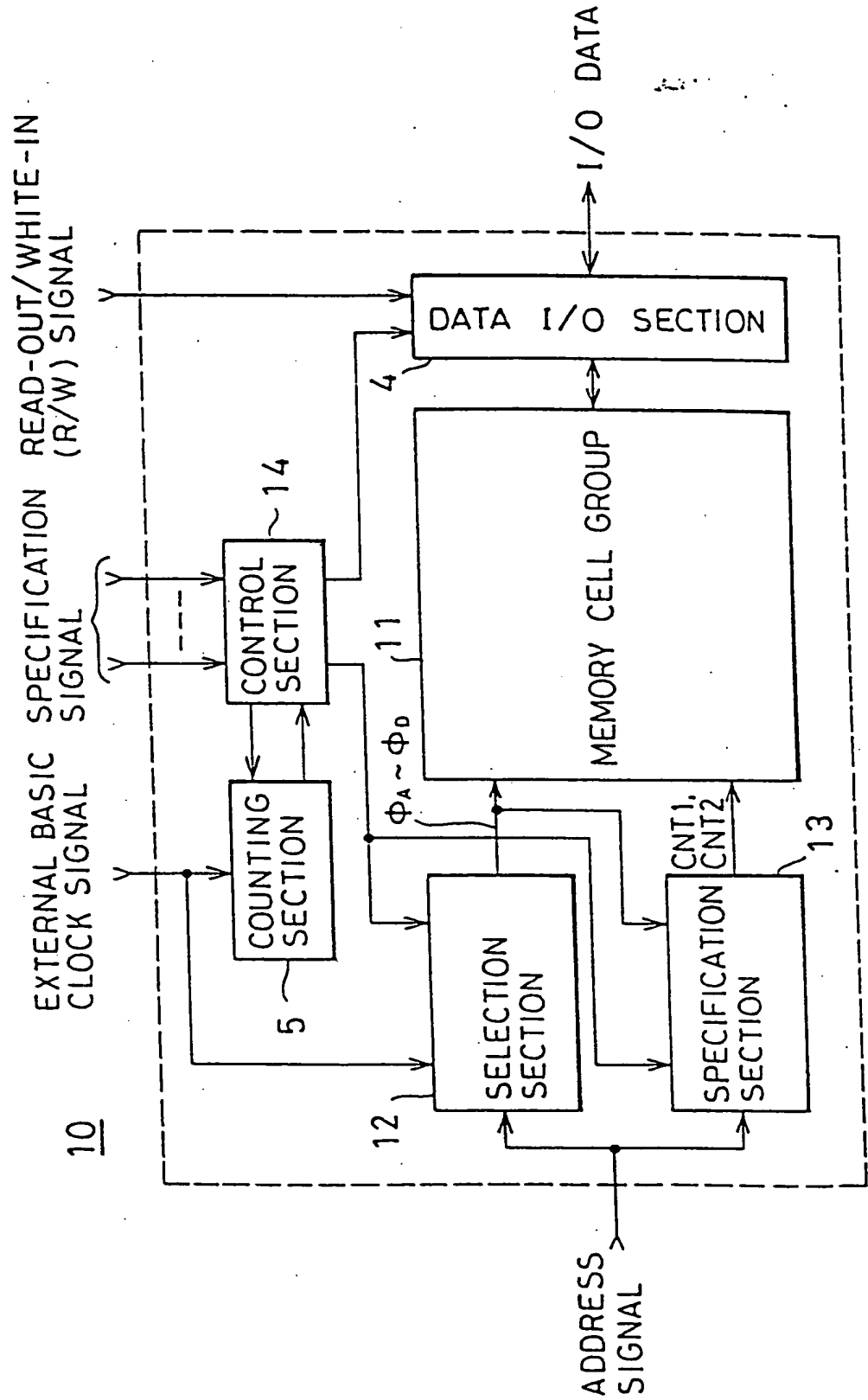


FIG. 7

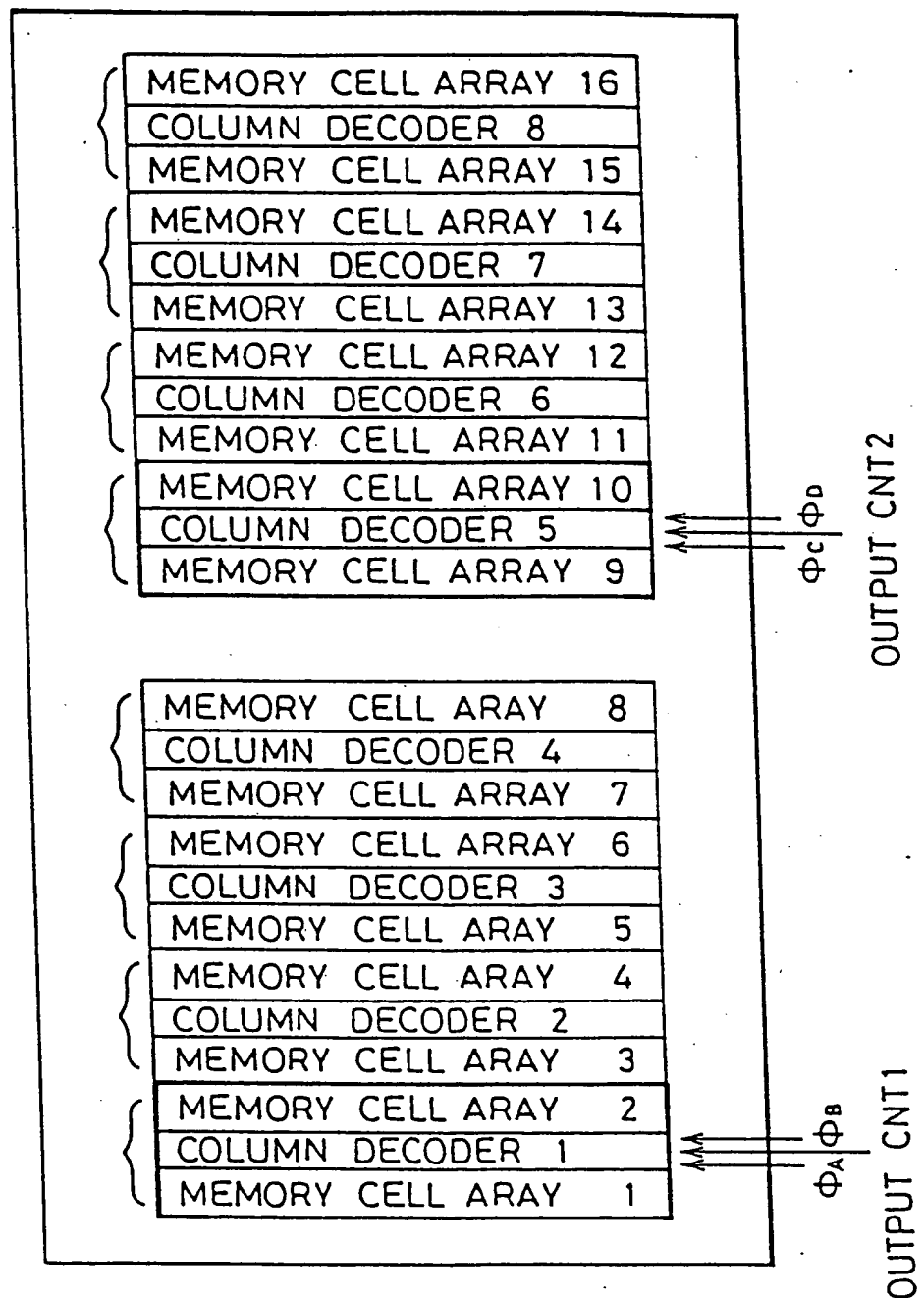


FIG. 8

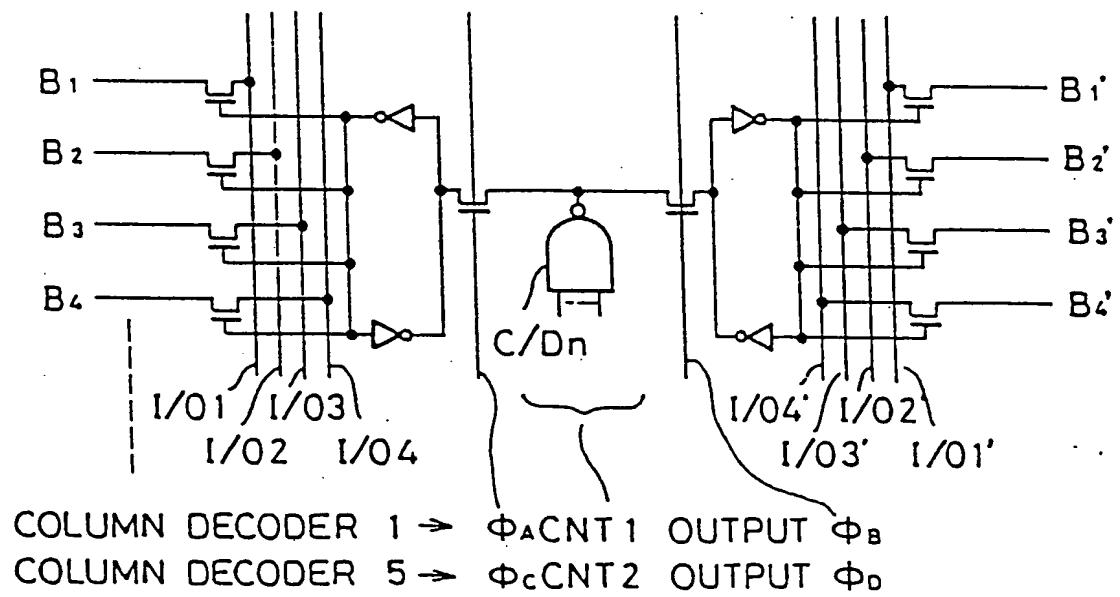
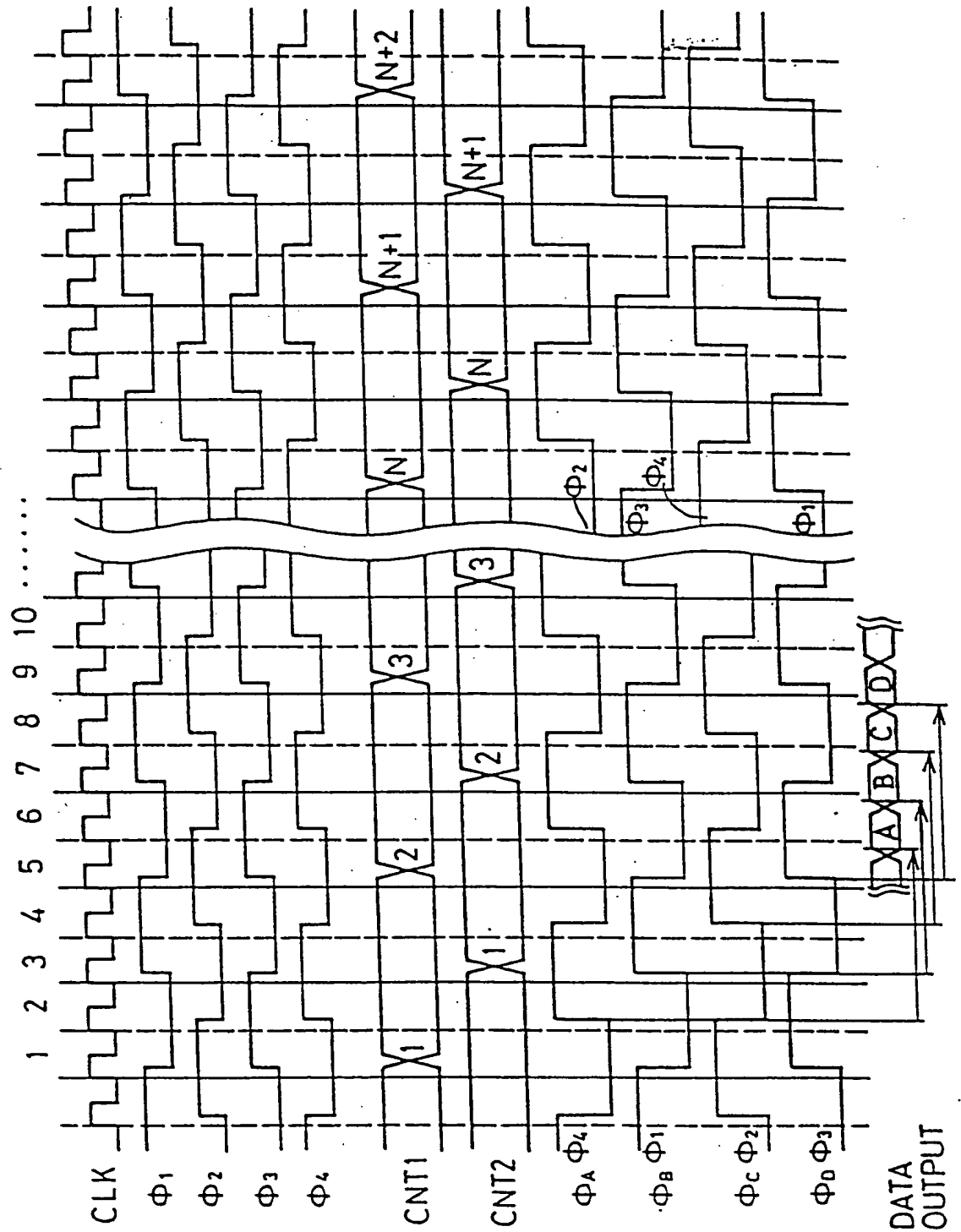


FIG. 9



4. .



FIG. 12A

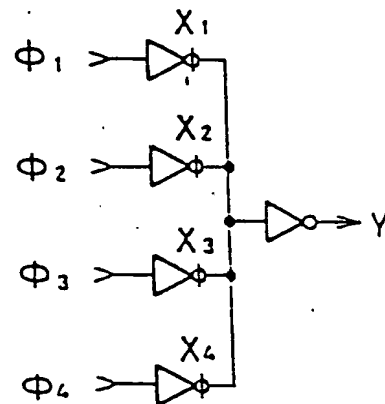


FIG. 12B

Y	X ₁	X ₂	X ₃	X ₄
Φ _A	A	B	C	D
Φ _B	D	A	B	C
Φ _C	C	D	A	B
Φ _D	B	C	D	A

FIG. 13A

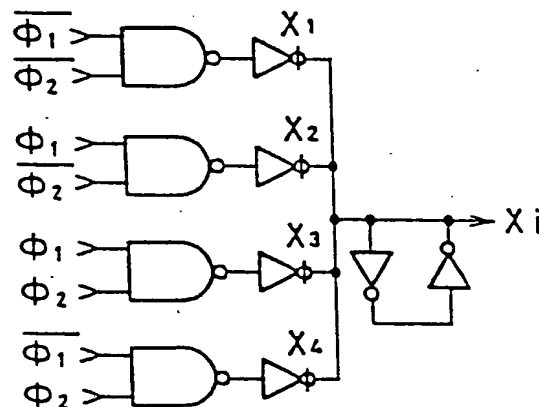


FIG. 13B

X _i	X ₁	X ₂	X ₃	X ₄
A	α	β	γ	δ
B	δ	α	β	γ
C	γ	δ	α	β
D	β	γ	δ	α

FIG. 14

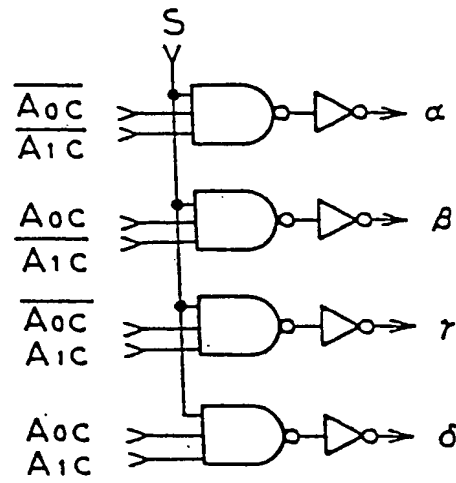
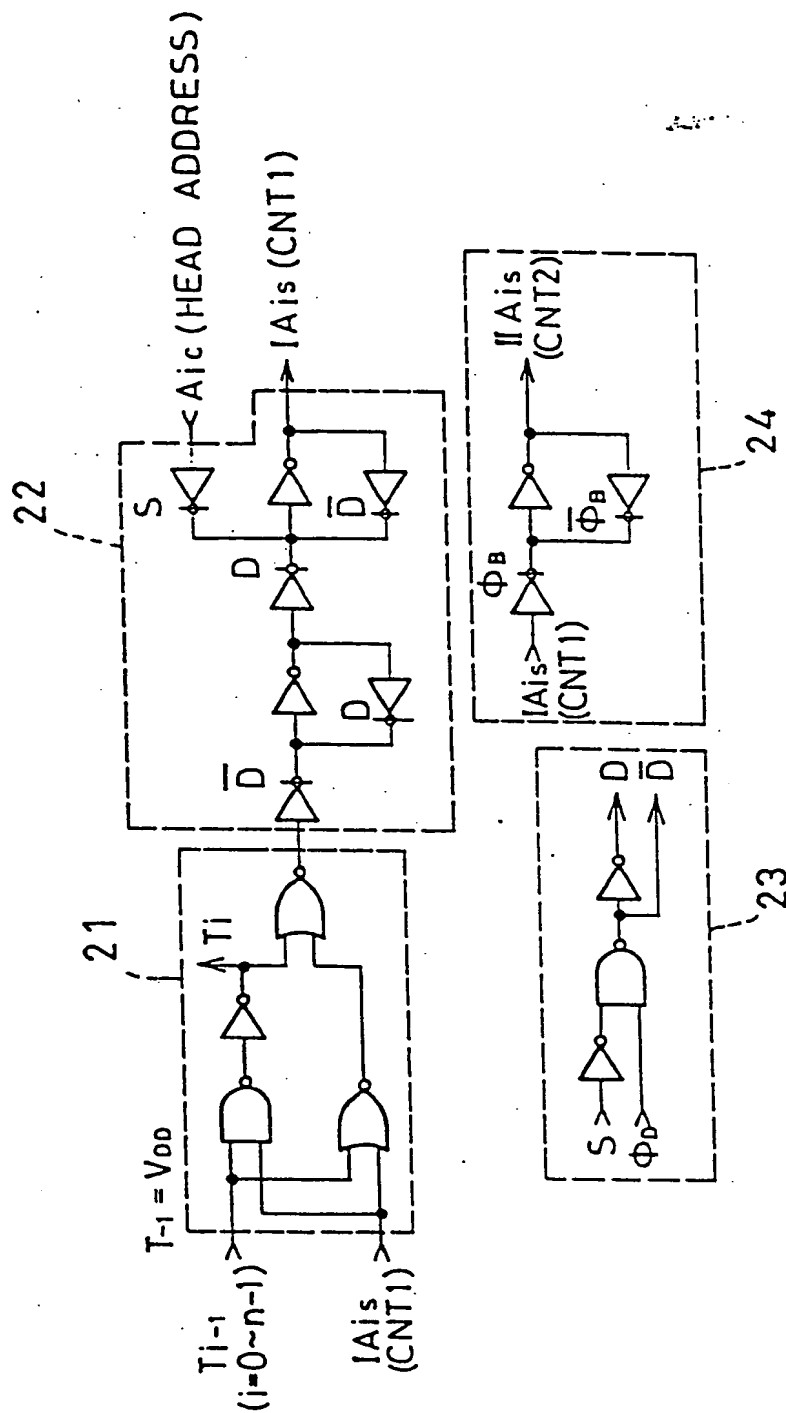


FIG. 15



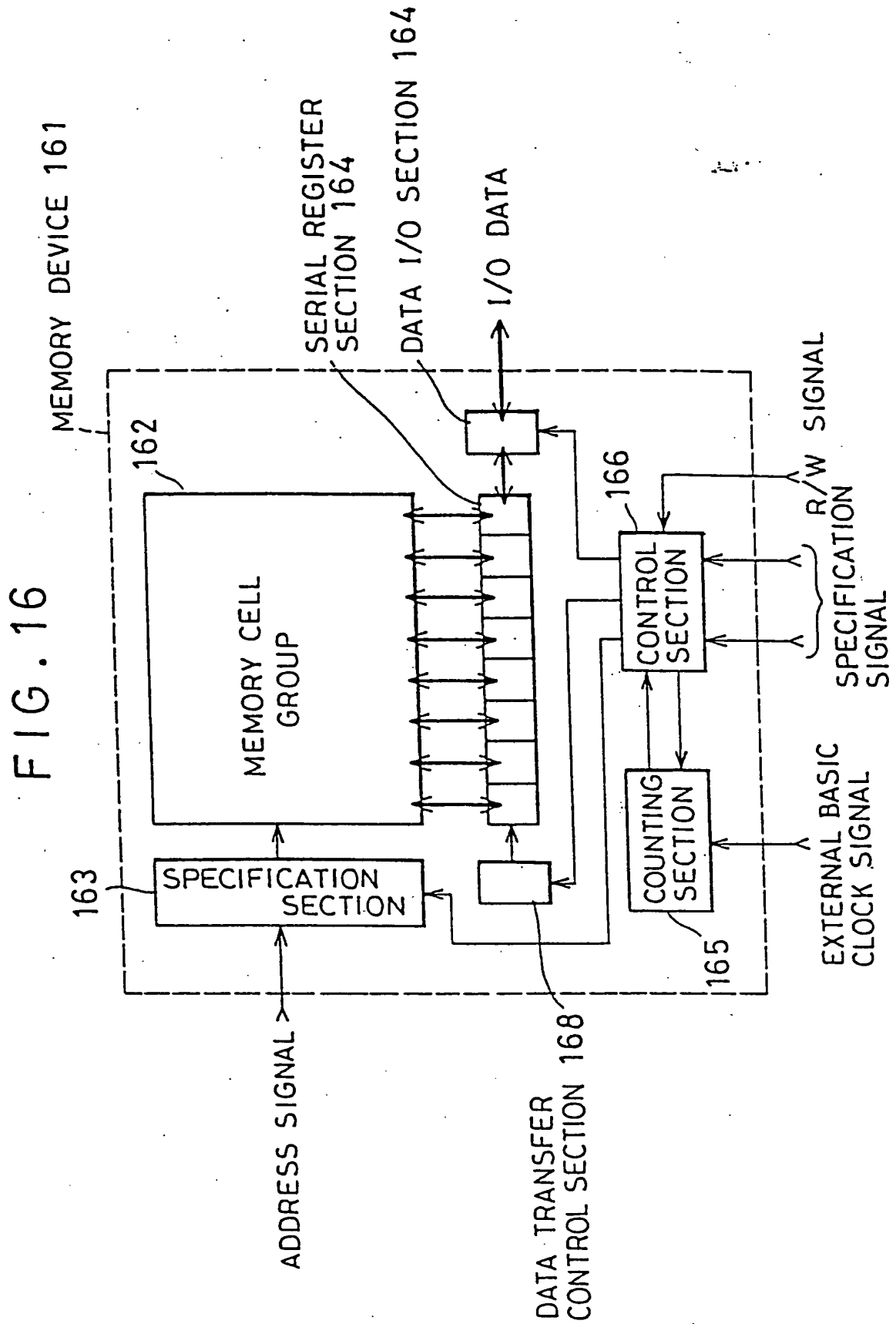
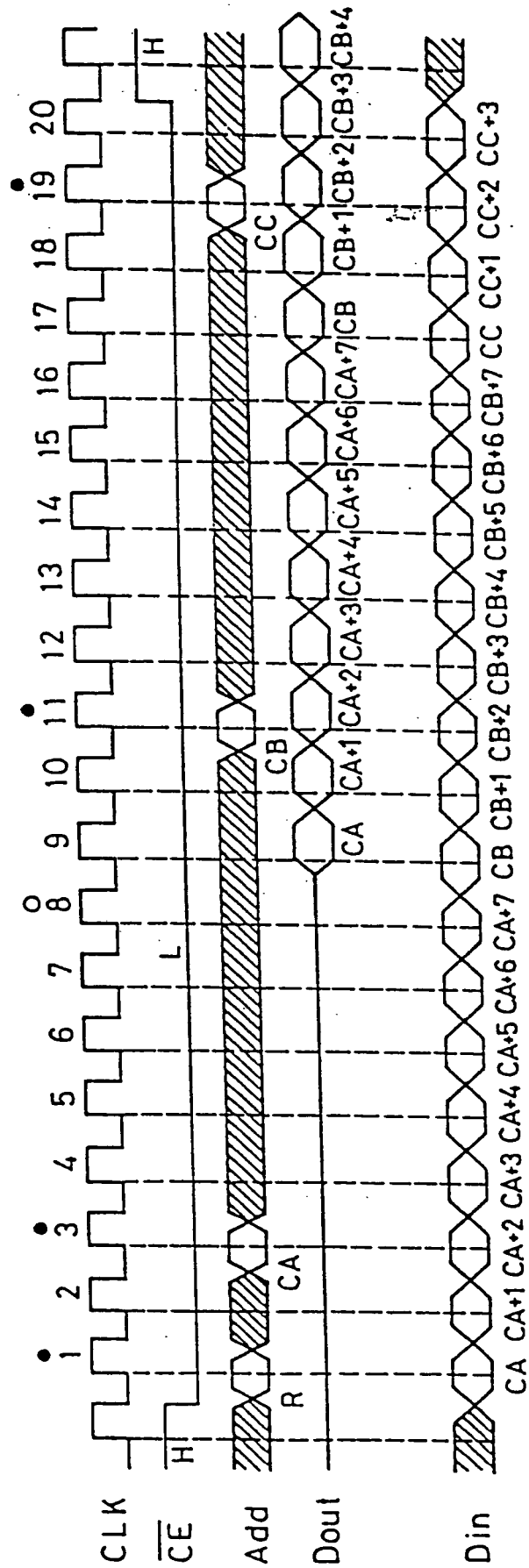


FIG. 17

● ADDRESS FETCH

○ DATA OUTPUT START



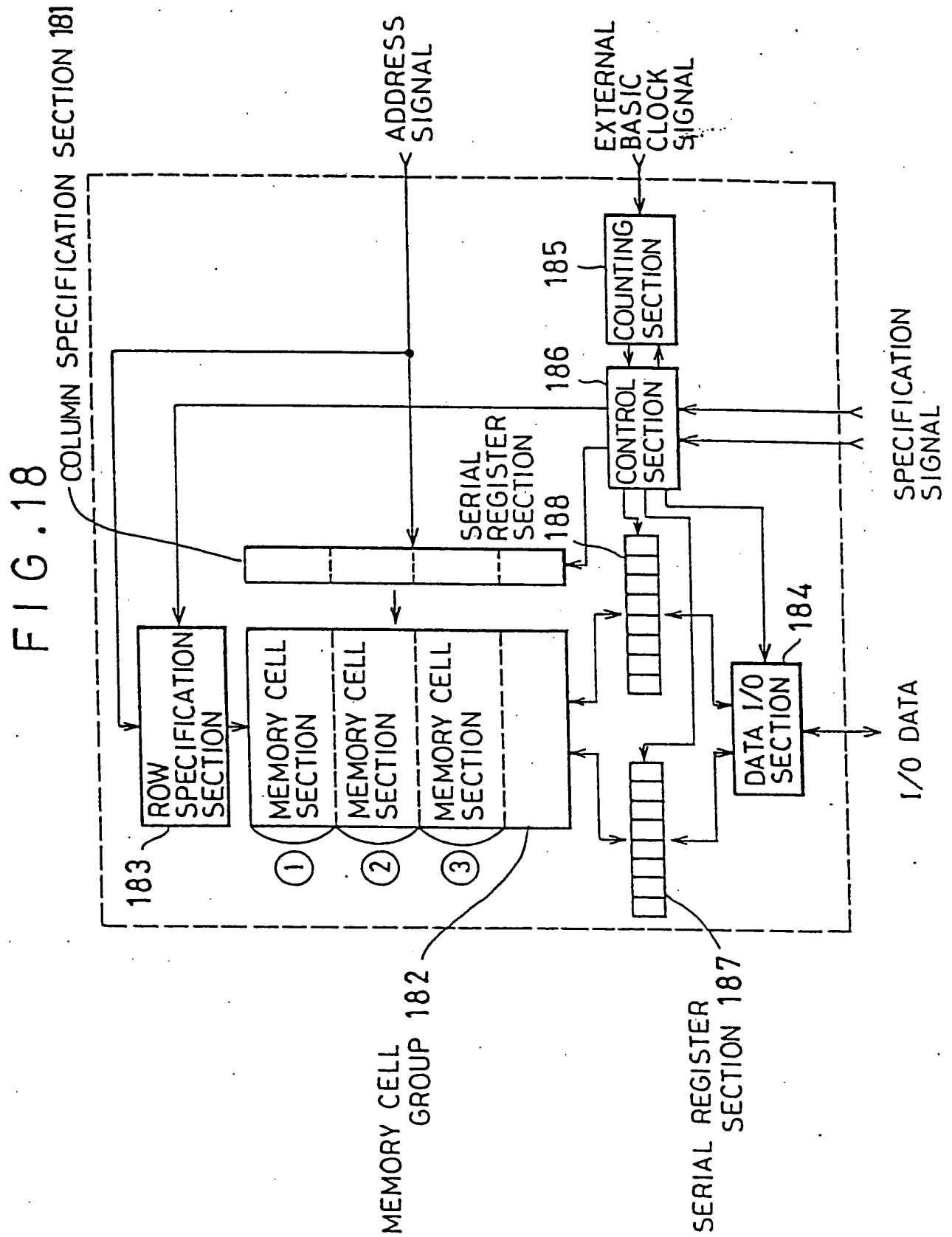


FIG. 19

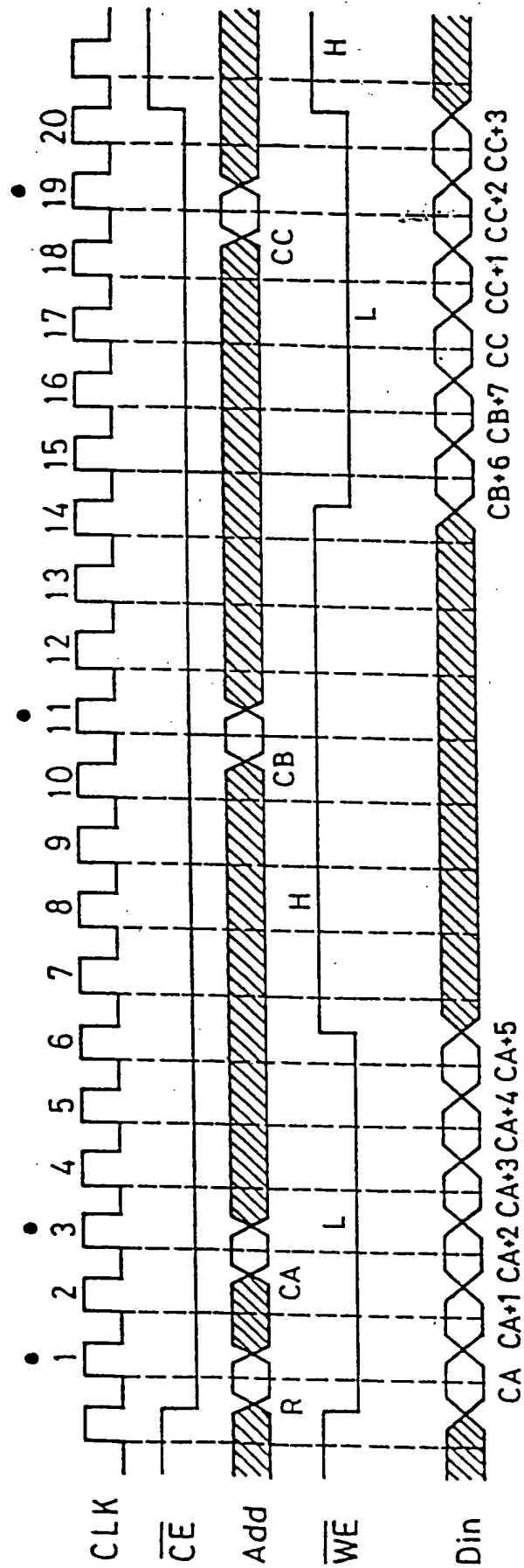


FIG. 20

